U.S. Department of Commerce Attorney D ck t No. 0756-1961 Serial No. N tY tAssign d Form PTO: 1449 Patent and Trademark Office (Rev. 8-83) INFORMATION DISCLOSURE STATEMENT Applicant: Shunpei YAMAZAKI tal. (Use several sheets if necessary) Filing Date: Group: 2812 U.S. PATENT DOCUMENTS Class Subclass Filing Date **Document Number** Date Name Examiner (if appr priate) Initial 2/23/88 Yamazaki Loke 4,727,044 Yamazaki 9/25/90 4,959,700 5,142,344 5/24/91 Yamazaki 11/15/83 Naem et al. 4,415,383 6/18/85 **Nishimura** 4,523,962 2/14/84 Teng 4,431,459 7/25/89 Troxel et al. 4,851,363 2/3/88 9/25/90 Yamazaki 4,959,700 4/26/94 Masumo et al. 437 40 5/10/91 5,306,651 101 10/24/83 Calder et al. 437 4,561,906 12/31/85 6/29/81 5,091,334 2/25/92 Yamazaki et al. 437 101 257 66 6/13/90 12/21/93 Yamazaki 5,272,361 FOREIGN PATENT DOCUMENTS Subclass **Translation** Class **Document Number** Date Country Yes No Abstract 59-121876 7/14/84 Japan 12/4/85 **Abstract** Japan 60-245174 Abstract 6-59280 3/4/94 **Japan** Abstract 5-206468 8/13/93 Japan 7/27/89 Abstract 1-187983 Japan OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) Sameshima et al., Japanese Journal of Applied Physics, "XeCI Excimer Laser Annealing Used to Loke Fabricate Poly-Si TFT's", Vol. 28, No. 10, October, 1989, p. 1789-1793. Sera et al., "High Performance TFT's Fabricated by XeCI Excimer Laser Annealing of Hydrogenated Amorphous Silicon Film", IEEE Transactions, 36 (1989), p. 2868-2872. Dat Considered Examiner *EXAMINER: Initial if citation consid red, wh ther or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not consid red. Includ copy of this form with n xt communicati n

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Sheet 2 of Form PT(-1449 U.S. Department of Commerce Attorney D cket N . 0756-1961 S rial No. Not Yet Assign d (Rev. 8 83) Patent and Trademark Office INFORMATION DISCLOSURE STATEMENT Applicant: Shunpei YAMAZAKI tal. ' (Use several sheets if necessary) Group: 2812 Filing Date: U.S. PATENT DOCUMENTS Examiner **Document Number** Date Name Class Subclass Filing Date Initial (if appr priate) Kosaka 12/31/91 437 72 1/16/90 5,077,235 oke. 4/26/94 Masumo et al. 437 40 5/10/91 5,306,651 3/21/89 101 6/19/87 Sasaki et al. 437 4,814,292 5,298,434 3/29/94 Strater et al. 437 21 2/7/92 5/17/94 Yamazaki et al. 257 66 3/17/92 5,313,076 4,727,044 2/23/88 Yamazaki 437 45 FOREIGN PATENT DOCUMENTS **Document Number Date** Country Class **Subclass Translation** Yes No 0 023 429 5/82 Japan-437 978 0 178 447 4/86 437 173-Japan OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) Loke Kiang et al., "Modification of S/C Device Chas. by Lasers", IBM, 1982, p. 171-6. Sameshima et al., "XeCl Excimer Laser Annealing Used to Fabricate Poly-Si TFT's", Mat. Res Loke Soc. Symp Proc. Vol. 71, 1986, p. 435-440. Iwamatsu et al., "Silicon on Saaphire MOSFETs Fabricated by Back Surface Laser Anneal Loke Technology", Electronics Letters, Vol. 15, No. 25, p. 827-828, December, 1979. Examiner loke Date Consid red

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